

The diagram illustrates a computer system architecture. At the top, a scanner (S) is connected to a central unit (C). The central unit (C) is connected to a bus (B). The bus (B) is connected to multiple input devices (I) and a processor (R). The processor (R) is labeled "PROCESSOR".

Page 11 of 12

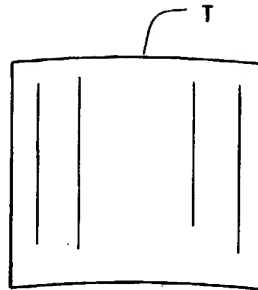


FIG. 3A



FIG. 3B



FIG. 3C